

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

|   |                               |
|---|-------------------------------|
| In re application of  | Docket No: Q64872             |
| Toshifumi SATO  | Group Art Unit: 2611          |
| Appln. No.: 09/875,151  | Examiner: Kevin Michael BURD  |
| Confirmation No.: 7262  | Patent No.: 7,142,587         |
| Filed: June 7, 2001   | Issue Date: November 28, 2006 |
| For: RECEIVED PATH TIMING DETECTING CIRCUIT AT DS-CDMA SYSTEM |                               |

**REQUEST FOR CERTIFICATE OF CORRECTION**

**ATTN: Certificate of Correction Branch**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

Pursuant to the provisions of 37 C.F.R. § 1.322, please enter the attached Certificate of Correction.

Since the errors noted are believed to be the fault of the Patent and Trademark Office, we are not enclosing the \$100.00 Certificate of Correction fee. If it is found to be to the contrary, please charge our Deposit Account No. 19-4880.

In view of the foregoing, issuance of the Certificate of Correction is respectfully requested.

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Respectfully submitted,  
/Howard L. Bernstein/  
Howard L. Bernstein  
Registration No. 25,665

WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: January 9, 2007

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**

PATENT NO: 7,142,587  
DATED: November 28, 2006  
INVENTOR(S): Toshifumi SATO

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1, line 65, delete "At" and insert --Δt--.

MAILING ADDRESS OF SENDER:  
**SUGHRUE MION, PLLC**

WASHINGTON OFFICE

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CUSTOMER NUMBER

PATENT NO. 7,142,587

No. of additional copies